

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Steven J. Koester

Examiner: Anh D. Mai

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For: SEMICONDUCTOR FIELD-EFFECT
TRANSISTOR HAVING STRAINED-LAYER
(As Amended)

Dated: February 23, 2009

Confirm. No: 7401

Commissioner of Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.111

Sir:

In response to the Office Action dated November 26, 2008, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on February 23, 2009.

Dated: February 23, 2009



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